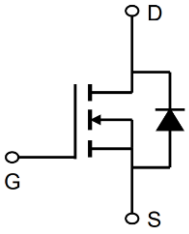
AP6N03SI

30V N-Channel Enhancement Mode MOSFET

Description

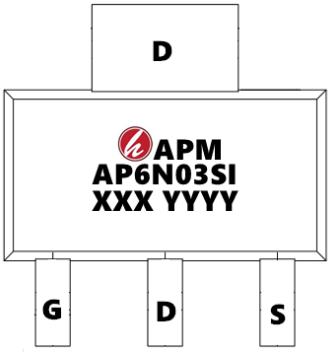
The AP6N03SI uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 2.5V. This device is suitable for use as a Battery protection or in other Switching application.



General Features

V_{DS} = 30V I_D =6.3A

$R_{DS(ON)}$ < 35mΩ @ V_{GS}=4.5V （**Type：24mΩ**）

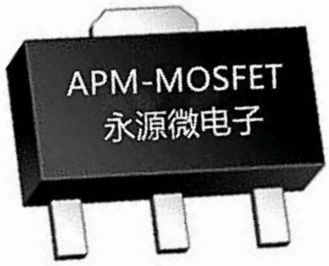


Application

3.3V MCU

Load switch

Uninterruptible power supply



Package Marking and Ordering Information

Product ID	Pack	Marking	Qty(PCS)
AP6N03SI	SOT89-3L	AP6N03SI XXX YYYY	3000

Absolute Maximum Ratings (T_C=25℃unless otherwise noted)

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	30	V
V_{GS}	Gate-Source Voltage	±12	V
I_D@T_A=25℃	Continuous Drain Current	6.3	A
I_D@T_A=70℃	Continuous Drain Current	4.7	A
I_{DM}	Pulsed Drain Current[2]	30	A
P_D@T_A=25℃	Total Power Dissipation[3]	1.5	W
T_{STG}	Storage Temperature Range	-55 to 150	℃
T_J	Operating Junction Temperature Range	-55 to 150	℃
$R_{\theta JA}$	Thermal Resistance Junction-ambient [1]	85	℃/W
$R_{\theta JA}$	Thermal Resistance Junction-Ambient [1] (t ≤10s)	30	℃/W

Electrical Characteristics (T_J=25 ℃, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BVDSS	Drain-Source Breakdown Voltage	V_{GS}=0V , I_D=250uA	30	33	---	V
△BVDSS/△TJ	BVDSS Temperature Coefficient	Reference to 25℃ , I_D=1mA	---	0.029	---	V/℃
RDS(ON)	Static Drain-Source On-Resistance[2]	V_{GS}=10V , I_D=5A	---	22	26	mΩ
		V_{GS}=4.5V , I_D=3A	---	24	35	
		V_{GS}=2.5V , I_D=1A	---	36	40	
VGS(th)	Gate Threshold Voltage	V_{GS}=V_{DS} , I_D =250uA	0.5	0.9	1.2	V
△$V_{GS(th)}$	$V_{GS(th)}$ Temperature Coefficient		---	-2.82	---	mV/℃
IDSS	Drain-Source Leakage Current	V_{DS}=24V , V_{GS}=0V , T_J=25℃	---	---	1	uA
		V_{DS}=24V , V_{GS}=0V , T_J=55℃	---	---	5	
IGSS	Gate-Source Leakage Current	V_{GS}=±12V , V_{DS}=0V	---	---	±100	nA
gfs	Forward Transconductance	V_{DS}=5V , I_D=5A	---	25	---	S
R_g	Gate Resistance	V_{DS}=0V , V_{GS}=0V , f=1MHz	---	1.5	---	Ω
Q_g	Total Gate Charge (4.5V)	V_{DS}=15V , V_{GS}=4.5V , I_D=5.8A	---	11.5	---	nC
Q_{gs}	Gate-Source Charge		---	1.6	---	
Qgd	Gate-Drain Charge		---	2.9	---	
Td(on)	Turn-On Delay Time	V_{DD}=15V , V_{GS}=10V , R_G=3Ω I_D=5A	---	5	---	ns
T_r	Rise Time		---	47.	---	
Td(off)	Turn-Off Delay Time		---	26	---	
T_f	Fall Time		---	8	---	
C_{iss}	Input Capacitance	V_{DS}=15V , V_{GS}=0V , f=1MHz	---	530	---	pF
Coss	Output Capacitance		---	130	---	
Crss	Reverse Transfer Capacitance		---	36	---	
I_S	Continuous Source Current[1,4]	V_G=V_D=0V , Force Current	---	---	5.8	A
VSD	Diode Forward Voltage[2]	V_{GS}=0V , I_S=1A , T_J=25℃	---	---	1.2	V

Note :

1、The data tested by surface mounted on a 1 inch^2 FR-4 board with 2OZ copper.

2、The data tested by pulsed , pulse width ≦ 300us , duty cycle ≦ 2%

3、The power dissipation is limited by 150℃ junction temperature

4 、The data is theoretically the same as I_D and I_{DM} , in real applications , should be limited by total power dissipation.

Typical Characteristics

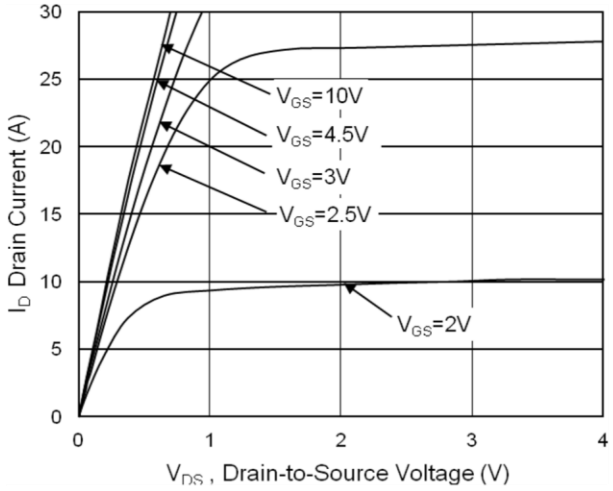


Fig.1 Typical Output Characteristics

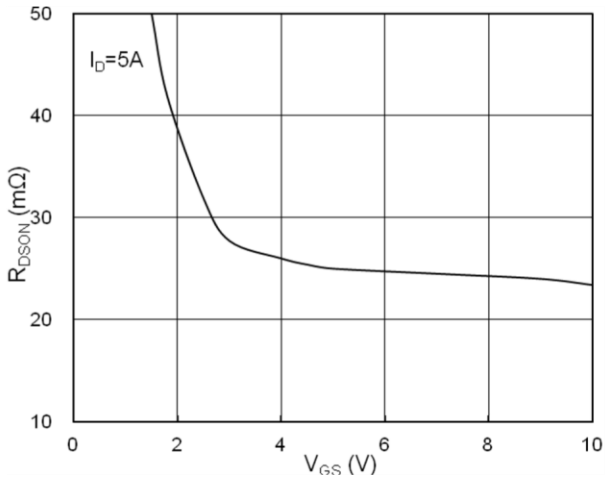


Fig.2 On-Resistance vs. Gate-Source

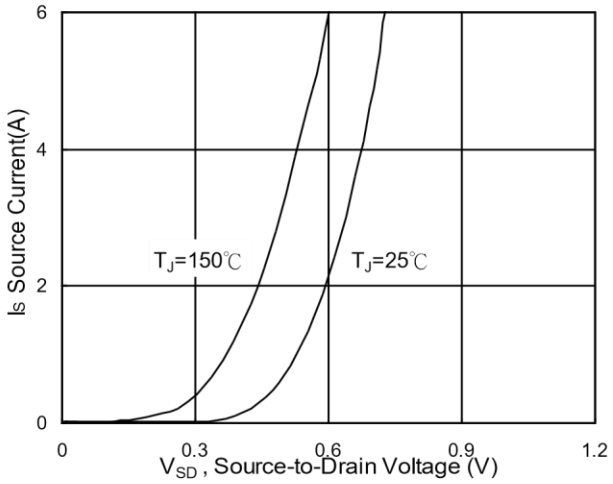


Fig.3 Forward Characteristics Of Reverse

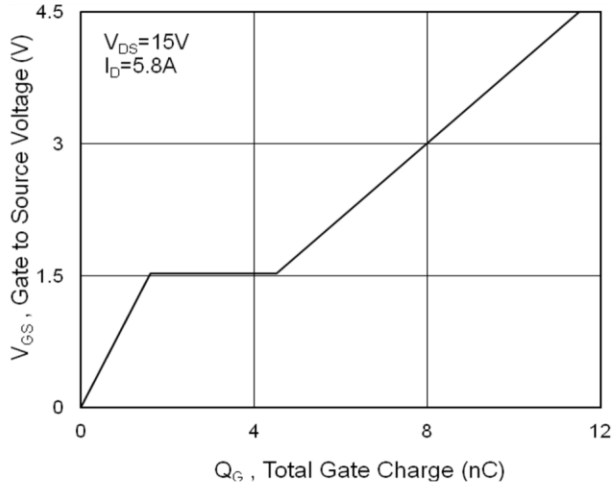


Fig.4 Gate-Charge Characteristics

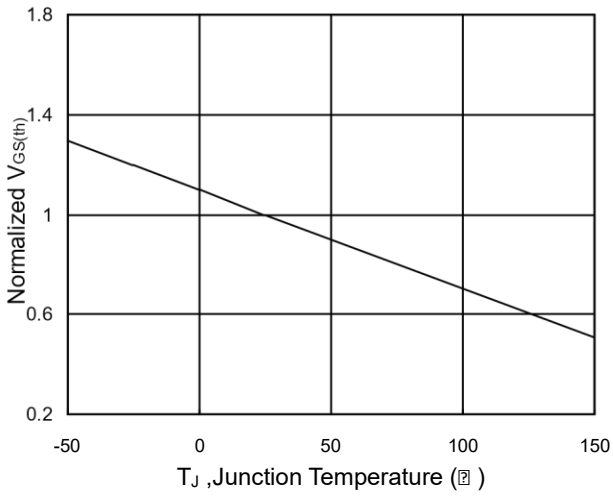


Fig.5 Normalized $V_{GS(th)}$ vs. T_J

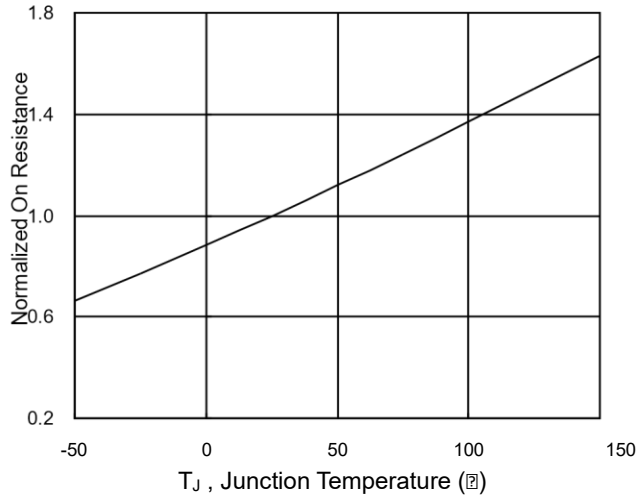


Fig.6 Normalized R_{DSON} vs. T_J

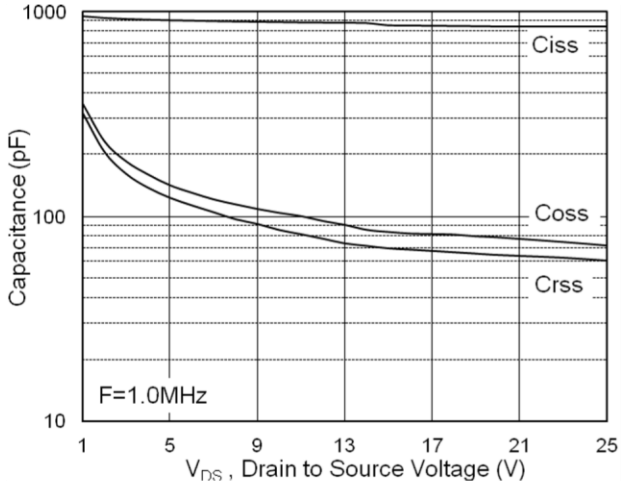


Fig.7 Capacitance

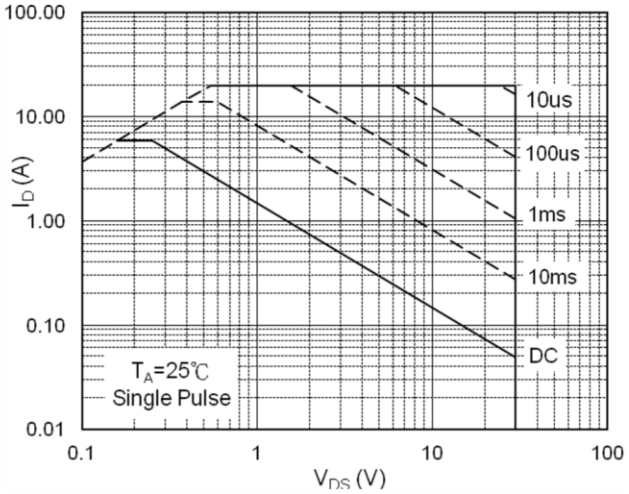


Fig.8 Safe Operating Area

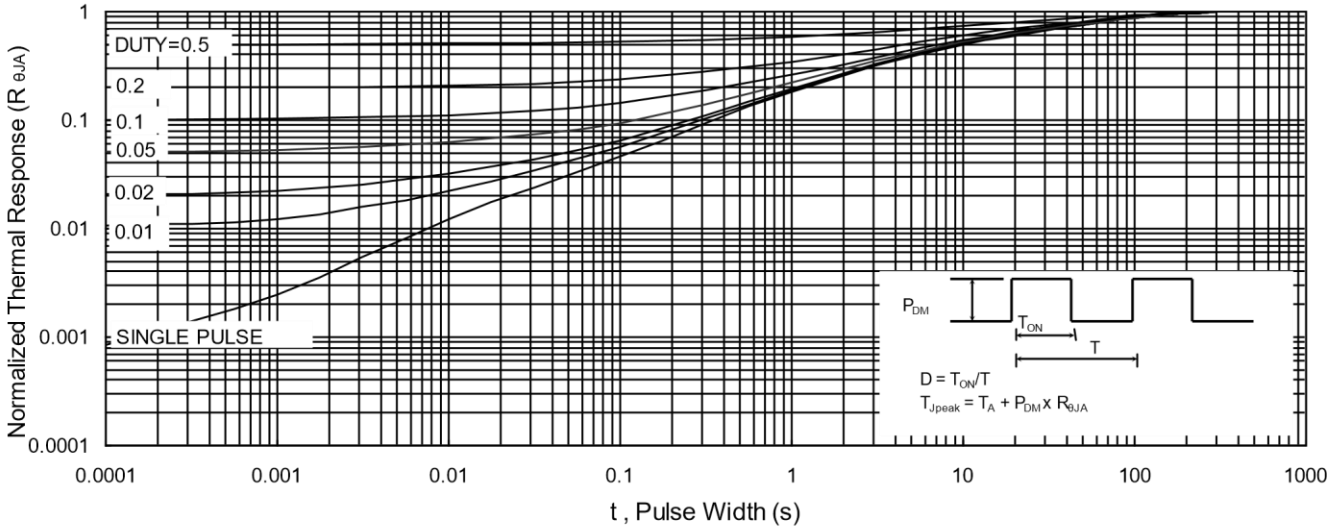


Fig.9 Normalized Maximum Transient Thermal Impedance

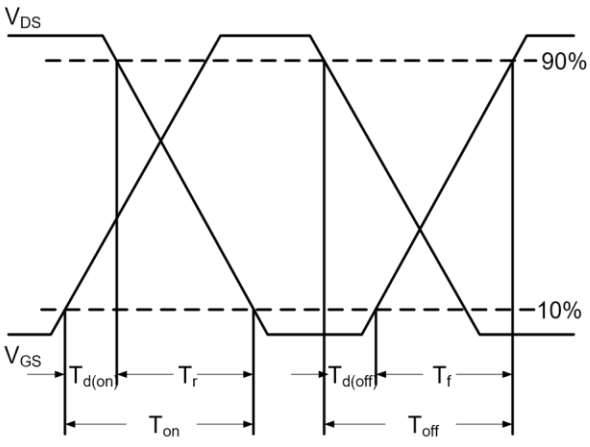


Fig.10 Switching Time Waveform

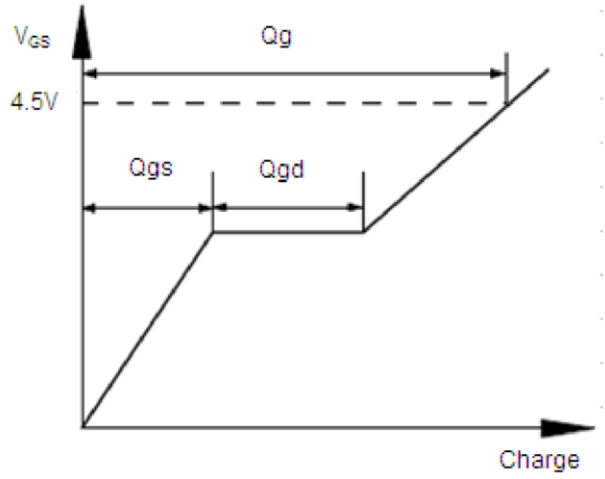
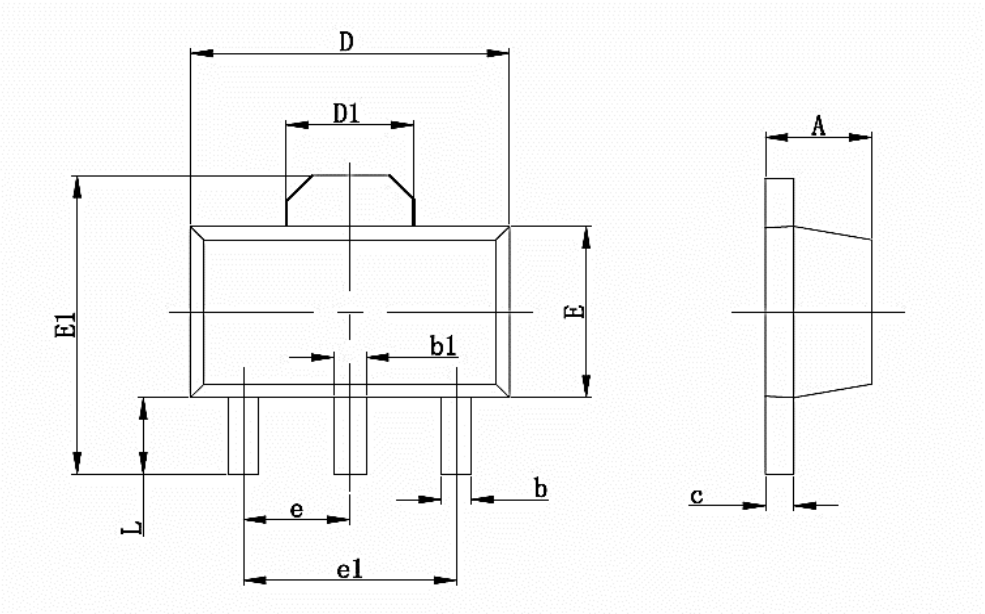


Fig.11 Gate Charge Waveform

Package Mechanical Data:SOT89-3L



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.400	1.600	0.055	0.063
b	0.350	0.520	0.013	0.197
b1	0.400	0.580	0.016	0.023
c	0.350	0.440	0.014	0.017
D	4.400	4.600	0.173	0.181
D1	1.550 REF		0.061 REF	
E	2.350	2.550	0.091	0.102
E1	3.940	4.250	0.155	0.167
e	1.500 TYP		0.060TYP	
e1	3.000 TYP		0.118TYP	
L	0.900	1.100	0.035	0.047

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30V N-Channel Enhancement Mode MOSFET

Edition	Date	Change
Rve3.0	2017/5/1	Initial release
Rve3.1	2020/5/20	Change Marking TO AP6N03SI