

150V N-Channel Enhancement Mode MOSFET

Description

The AP240N15P/T uses advanced **APM-SGT₁** technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 10V. This device is suitable for use as a Battery protection or in other Switching application.

General Features

$V_{DS} = 150V$ $I_D = 240A$

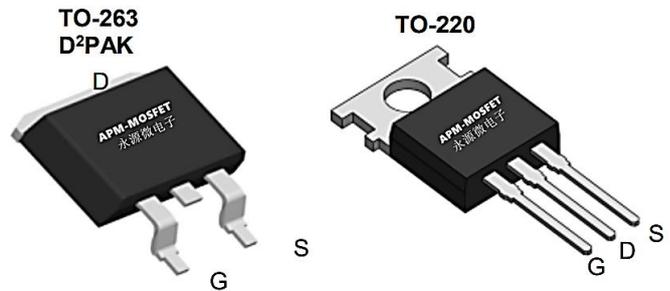
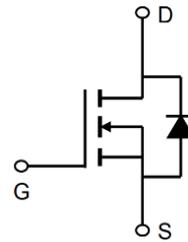
$R_{DS(ON)} < 5.8m\Omega$ @ $V_{GS}=10V$ (**Type: 4.8m Ω**)

Application

DC/DC Converter

LED Backlighting

Power Management Switches



Package Marking and Ordering Information

Product ID	Pack	Marking	Qty(PCS)
AP240N15P	TO-220-3L	AP240N15P XXX YYYY	1000
AP240N15T	TO-263-3L	AP240N15T XXX YYYY	800

Absolute Maximum Ratings ($T_C=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	150	V
V_{GS}	Gate-Source Voltage	± 20	V
$I_D@T_C=25^\circ\text{C}$	Continuous Drain Current, $V_{GS} @ 10V$	240	A
$I_D@T_C=100^\circ\text{C}$	Continuous Drain Current, $V_{GS} @ 10V$	185	A
IDM	Pulsed Drain Current	720	A
EAS	Single Pulse Avalanche Energy	1764	mJ
IAS	Avalanche Current	64	A
$P_D@T_C=25^\circ\text{C}$	Total Power Dissipation ⁴	326	W
TSTG	Storage Temperature Range	-55 to 150	$^\circ\text{C}$
T_J	Operating Junction Temperature Range	-55 to 150	$^\circ\text{C}$
$R_{\theta JA}$	Thermal Resistance Junction-Ambient	0.46	$^\circ\text{C/W}$
$R_{\theta JC}$	Thermal Resistance Junction-Case	62	$^\circ\text{C/W}$



150V N-Channel Enhancement Mode MOSFET

Electrical Characteristics (T_c=25°C unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
BVDSS	Drain-Source Breakdown Voltage	V _{GS} =0V I _D =250μA	150	165		V
IDSS	Zero Gate Voltage Drain Current	V _{DS} =140V, V _{GS} =0V			1	μA
IGSS	Gate-Body Leakage Current	V _{GS} =±20V, V _{DS} =0V			±100	nA
VGS(th)	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250μA	2.0	2.9	4.0	V
GFS	Forward Transconductance	V _{DS} =5V, I _D =15A		33		S
RDS(ON)	Drain-Source On-State Resistance	V _{GS} =10V, I _D =40A		4.8	5.8	mΩ
Ciss	Input Capacitance	V _{DS} =25V, V _{GS} =0V, f=1.0MHz		4200		pF
Coss	Output Capacitance			2867		pF
Crss	Reverse Transfer Capacitance			215		pF
td(on)	Turn-on Delay Time	V _{GS} =10V, V _{DS} =75V, RL=1.07Ω, RGEN=3Ω		18		nS
tr	Turn-on Rise Time			22		nS
td(off)	Turn-Off Delay Time			35		nS
tr	Turn-Off Fall Time			10		nS
Qg	Total Gate Charge	V _{GS} =10V, V _{DS} =75V, I _D =70A		65		nC
Qgs	Gate-Source Charge			20		nC
Qgd	Gate-Drain Charge			19		nC
ISD	Source-Drain Current (Body Diode)				240	A
VSD	Forward on Voltage ^(Note 3)	V _{GS} =0V, I _S =20A			1.2	V
trr	Reverse Recovery Time	I _F =20A, dI/dt=500A/μs		101		ns
Qrr	Reverse Recovery Charge	I _F =20A, dI/dt=500A/μs		1,240		nC

Notes:

- 1、 The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- 2、 The data tested by pulsed , pulse width ≤ 300μs , duty cycle ≤ 2%
- 3、 The EAS data shows Max. rating . The test condition is V_{DD}=50V, V_{GS}=10V, L=0.5mH, I_{AS}=64A
- 4、 The power dissipation is limited by 150°C junction temperature
- 5、 The data is theoretically the same as I_D and I_{DM} , in real applications , should be limited by total power dissipation.

Typical Characteristics

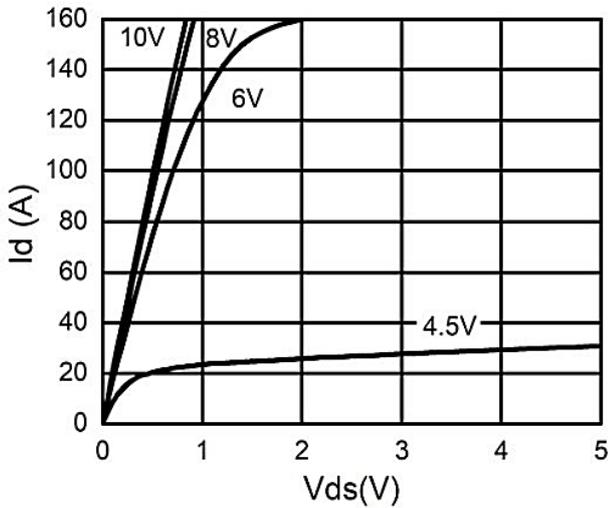


Figure 1. Output Characteristics

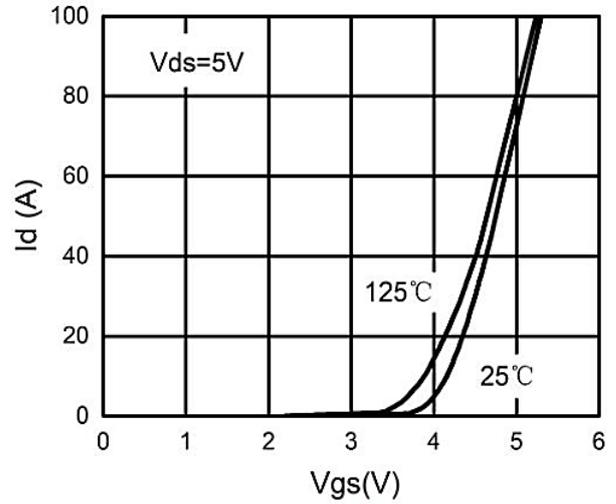


Figure 2. Transfer Characteristics

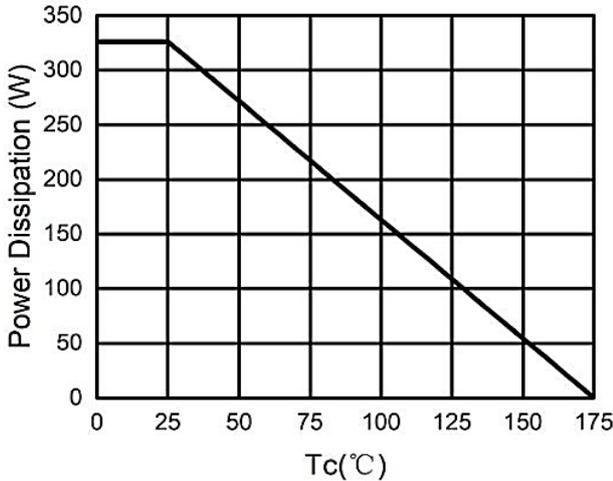


Figure 3. Power Dissipation

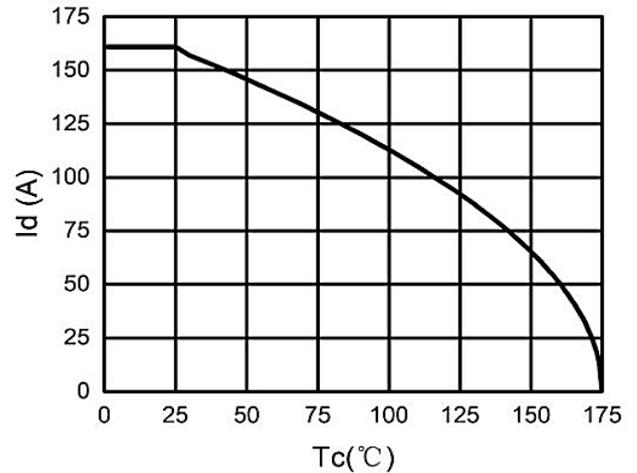


Figure 4. Drain Current

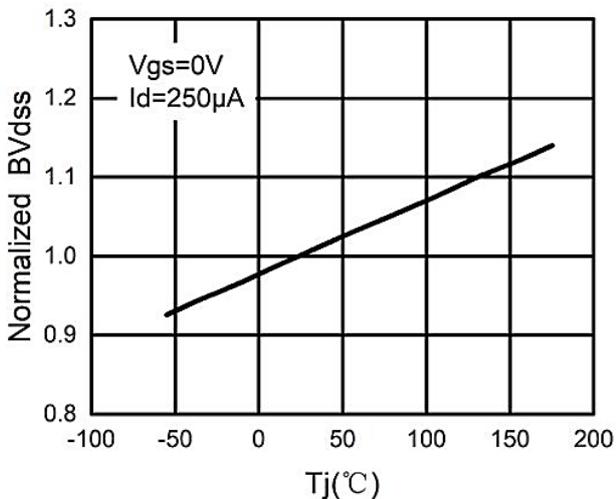


Figure 5. BVDSS vs Junction Temperature

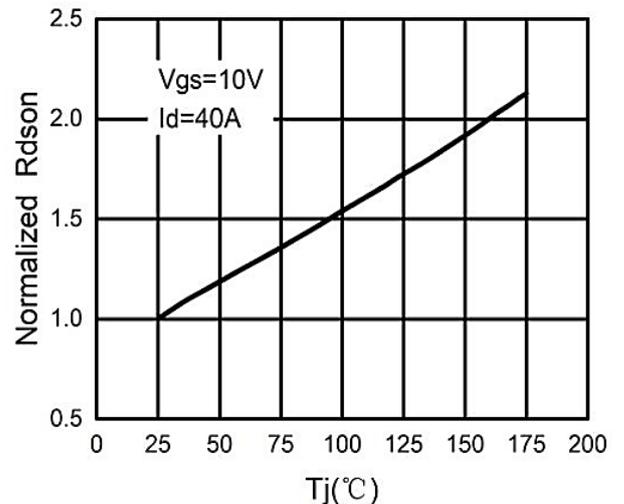


Figure 6. RDS(ON) vs Junction Temperature

150V N-Channel Enhancement Mode MOSFET

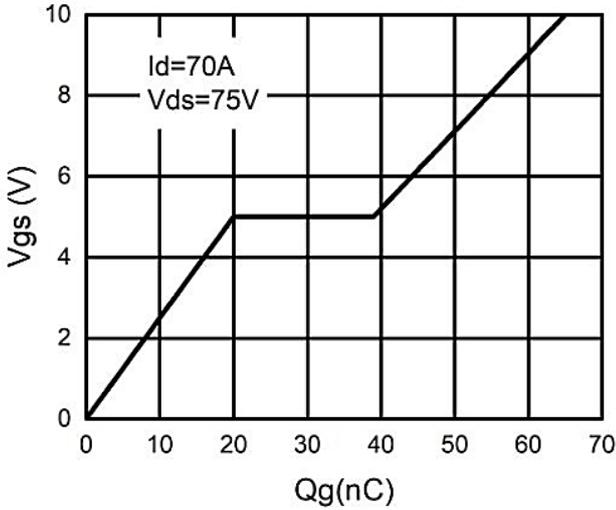


Figure 7. Gate Charge Waveforms

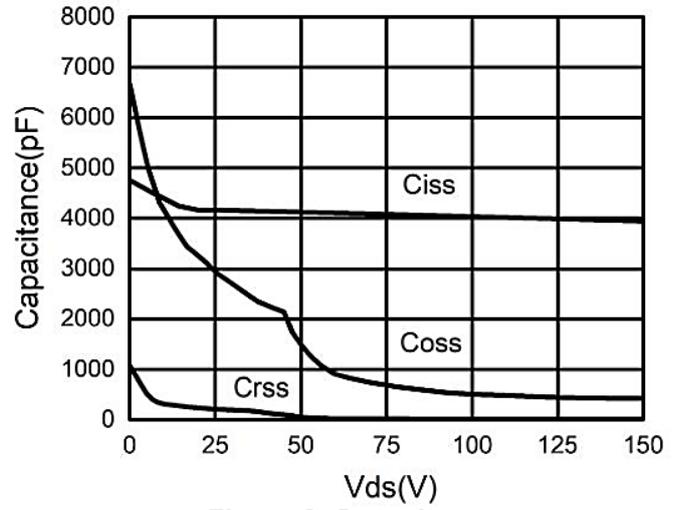


Figure 8. Capacitance

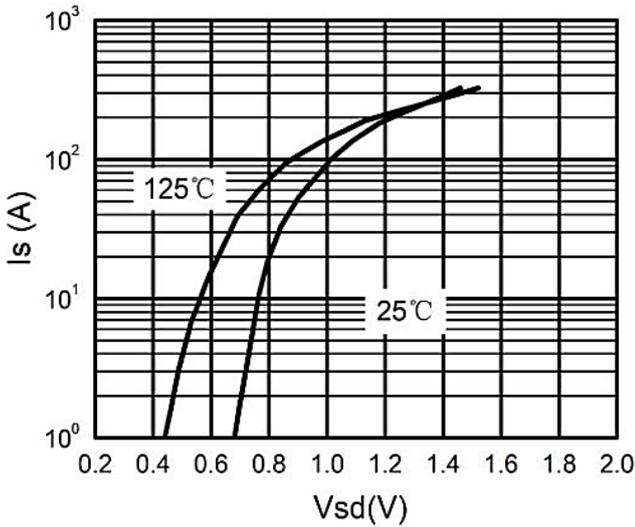


Figure 9. Body-Diode Characteristics

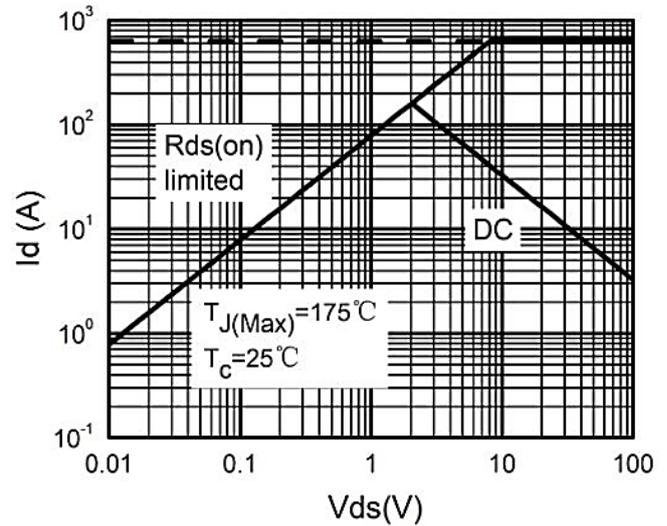


Figure 10. Maximum Safe Operating Area

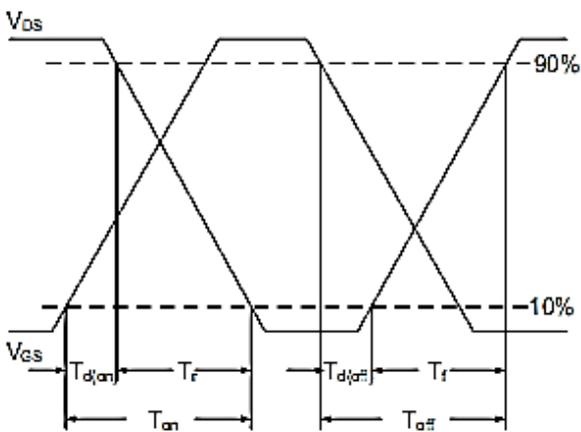


Figure 11. Switching Time Waveform

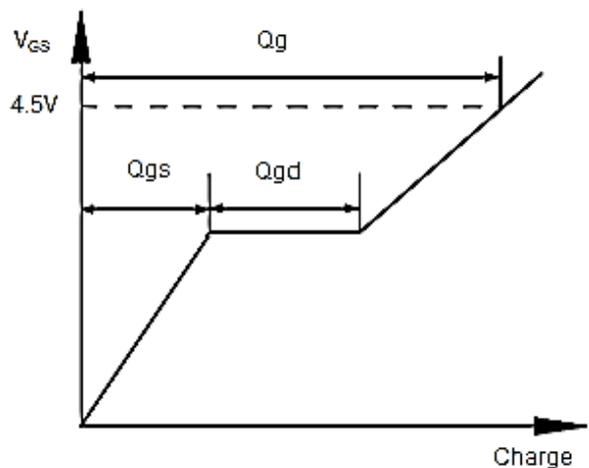
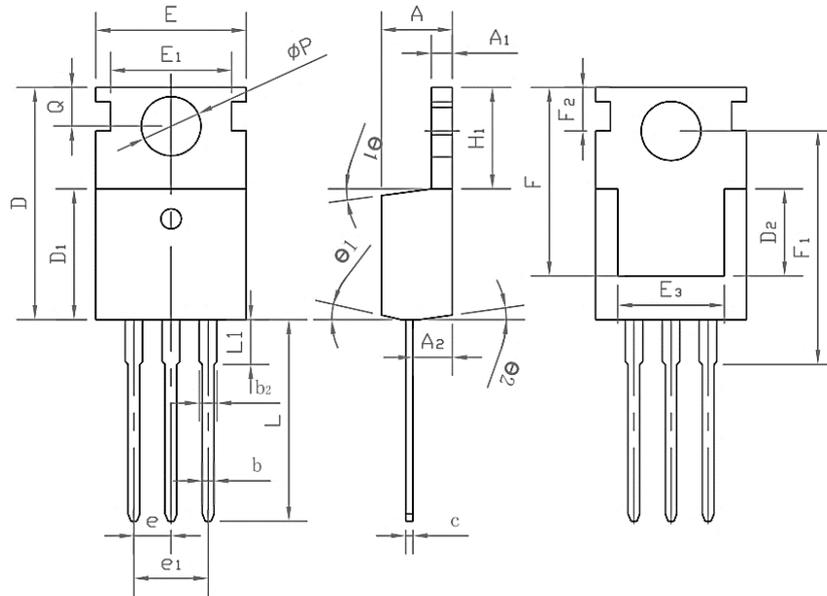


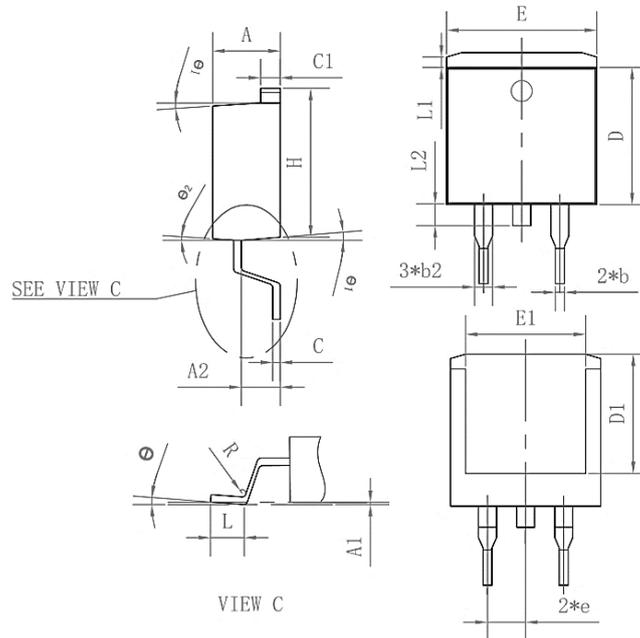
Figure 12. Gate Charge Waveform

Package Mechanical Data-TO-220-3L-SLK



Symbol	Common		
	mm		
	Mim	Nom	Max
A	4.27	4.57	4.87
A1	1.15	1.30	1.45
A2	2.10	2.40	2.70
b	0.70	0.80	1.00
b2	1.17	1.27	1.50
D	0.40	0.50	0.65
D1	8.80	9.10	9.40
D2	5.70	6.70	7.00
E	9.70	10.00	10.30
E1	-	8.70	-
E2	9.63	10.00	10.35
E3	7.00	8.00	8.40
e		0.37	
e1		0.10	
H1	6.00	6.50	6.85
L	12.75	13.50	13.90
L1	-	3.10	3.40
Φp	3.45	3.60	3.75
Q	2.60	2.80	3.00
θ1	4°	7°	10°
θ2	0°	3°	6°
F	13.30	13.50	13.70
F1	15.50	15.90	16.30
F2	2.80	3.00	3.20

Package Mechanical Data-TO-263-3L-SLK



Symbol	Common		
	mm		
	Mim	Nom	Max
A	4.35	4.47	4.60
A1	0.09	0.10	0.11
A2	2.30	2.40	2.70
b	0.70	0.80	1.00
b2	1.25	1.36	1.50
C	0.45	0.50	0.65
C1	1.29	1.30	9.40
D	9.10	9.20	9.30
D1	7.90	8.00	8.10
E	9.85	10.00	10.20
E1	7.90	8.00	8.10
H	15.30	15.50	15.70
e	-	2.54	-
L	2.34	2.54	2.74
L1	1.00	1.10	1.20
L2	1.30	1.40	1.50
R	0.24	0.25	0.26
θ	0°	4°	8°
θ1	4°	7°	10°
θ2	0°	3°	6°

150V N-Channel Enhancement Mode MOSFET**Attention**

1, Any and all APM Microelectronics products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical and/or material damage. Consult with your APM Microelectronics representative nearest you before using any APM Microelectronics products described or contained herein in such applications.

2, APM Microelectronics assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all APM Microelectronics products described or contained herein.

3, Specifications of any and all APM Microelectronics products described or contained here instipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer's products or equipment.

4, APM Microelectronics Semiconductor CO., LTD. strives to supply high quality high reliability products. However, any and all semiconductor products fail with some probability. It is possible that these probabilistic failures could give rise to accidents or events that could endanger human lives that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits and error prevention circuits for safe design, redundant design, and structural design.

5, In the event that any or all APM Microelectronics products (including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from the authorities concerned in accordance with the above law.

6, No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written permission of APM Microelectronics Semiconductor CO., LTD.

7, Information (including circuit diagrams and circuit parameters) herein is for example only; it is not guaranteed for volume production. APM Microelectronics believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.

8, Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. When designing equipment, refer to the "Delivery Specification" for the APM Microelectronics product that you intend to use.





Edition	Date	Change
Rve1.0	2021/8/5	Initial release

Copyright Attribution "APM-Microelectronice"

